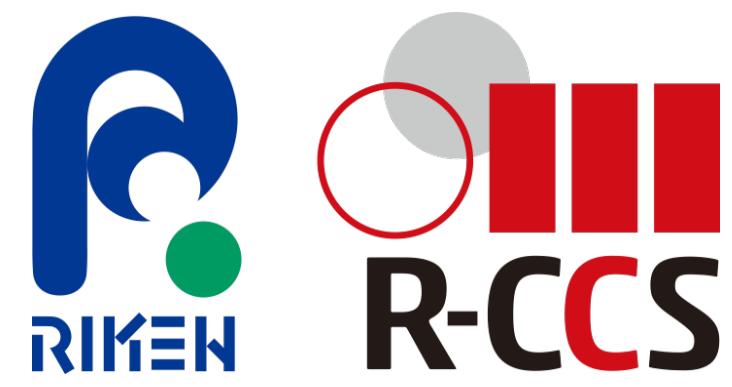
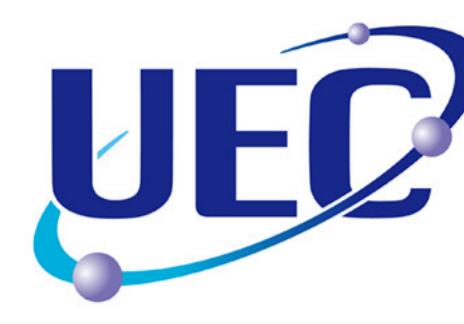


Rethinking the Bit Length of Post-Training Quantization for LLM Accuracy and Hardware Efficiency



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III Introduction

Background

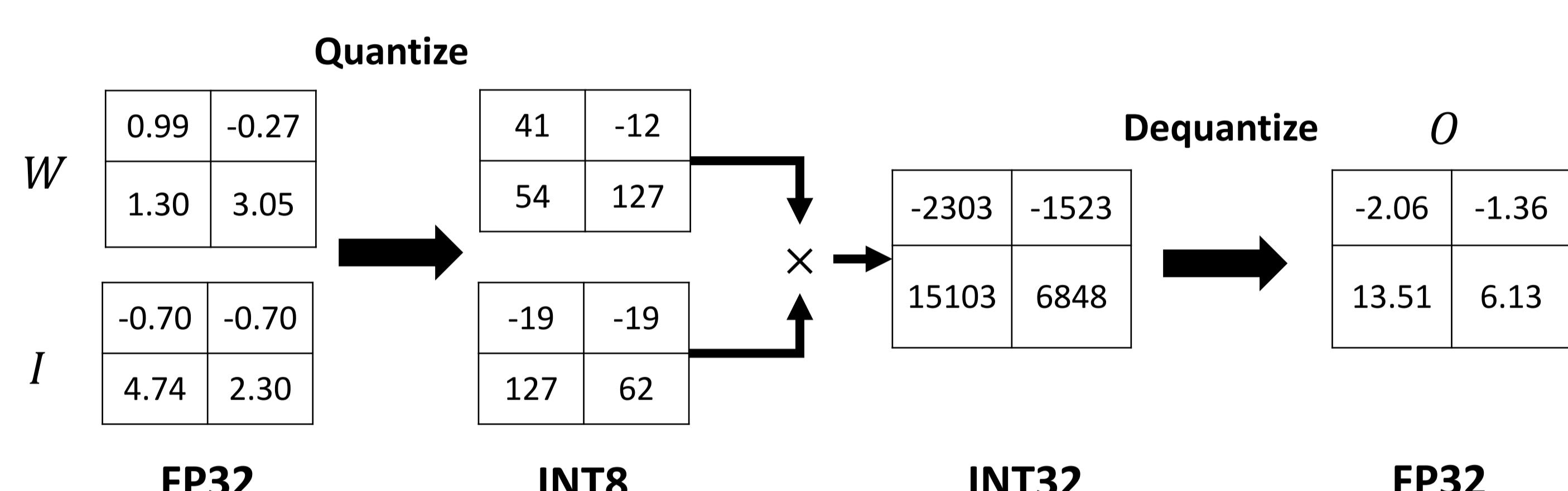
- Large language models (LLMs) consume too much memory and energy for inference
- Quantization [1] compresses model weights and activations from high precisions (e.g., FP32) to lower precisions (e.g., INT8, FP8)

Problem

- Standard 2-multiple bit lengths are often too coarse, providing a poor trade-off for preserving model accuracy.

Expected Result

- Provide fine-grain quantization precisions for higher LLM inference accuracy
- Achieve lower energy consumption or smaller area for the inference on hardware

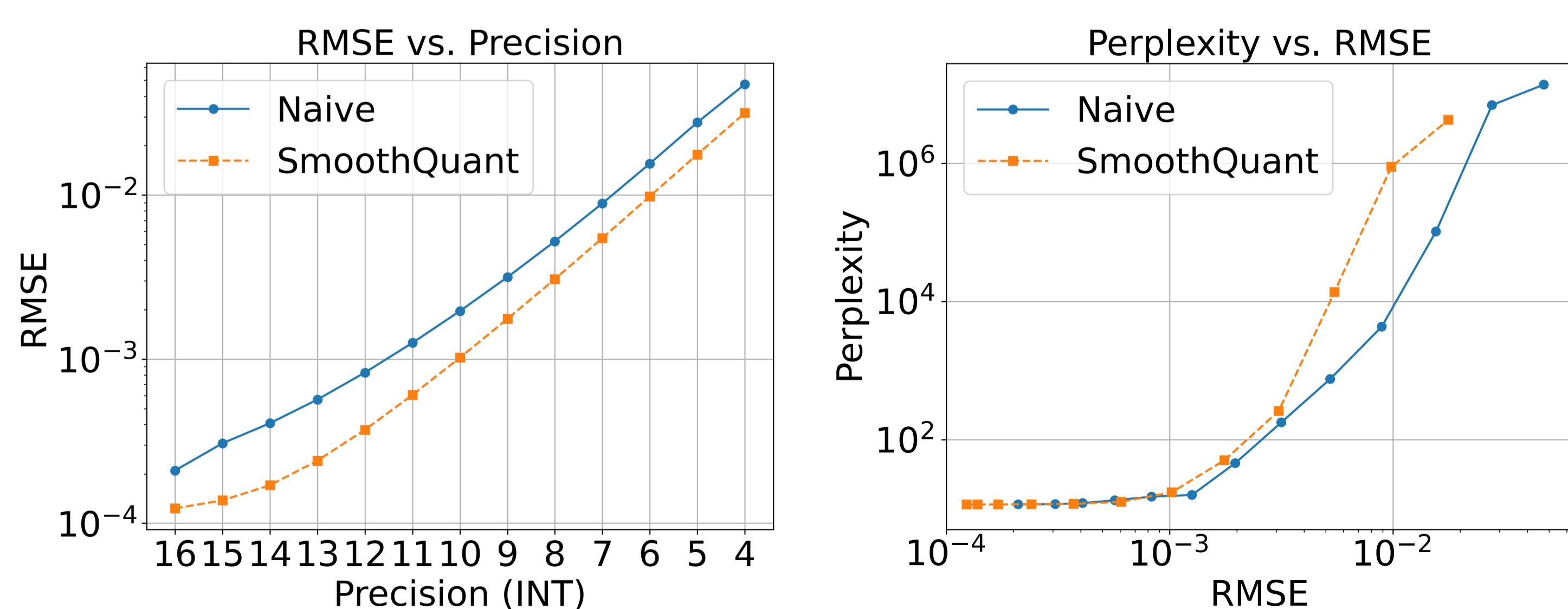


III Inference Accuracy

- Eight-bit precision (INT8) is conventionally considered sufficient to preserve the inference accuracy of quantized LLMs. However, our experiments challenge this assumption.

Experiment Settings

- Large language model: Llama-3.2-8B [2]
- Task: WikiText2 [3]
- Quantization methods:
 - Naïve: $X^q = \left\lfloor \frac{X}{\Delta} \right\rfloor, \Delta = \frac{\max(|X|)}{2^{N-1}-1}$
 - SmoothQuant [4]: Before applying naïve quantization, smooths the weights and activation by $X_s = X_{\text{diag}}(s)^{-s}, W_s = \text{diag}(s)W$
- Metrics:
 - Root mean square error (RMSE) for quantization error
 - Perplexity (cross-entropy loss) for inference accuracy



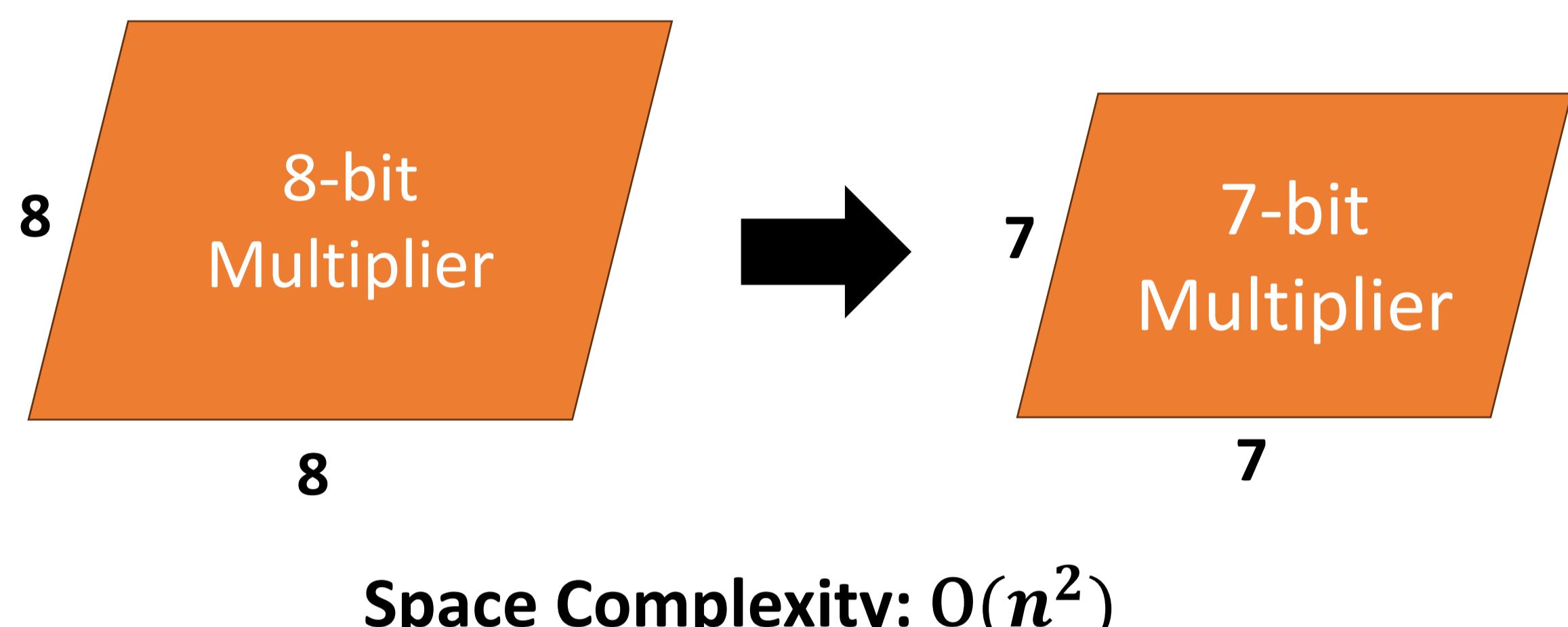
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III Hardware Implementation

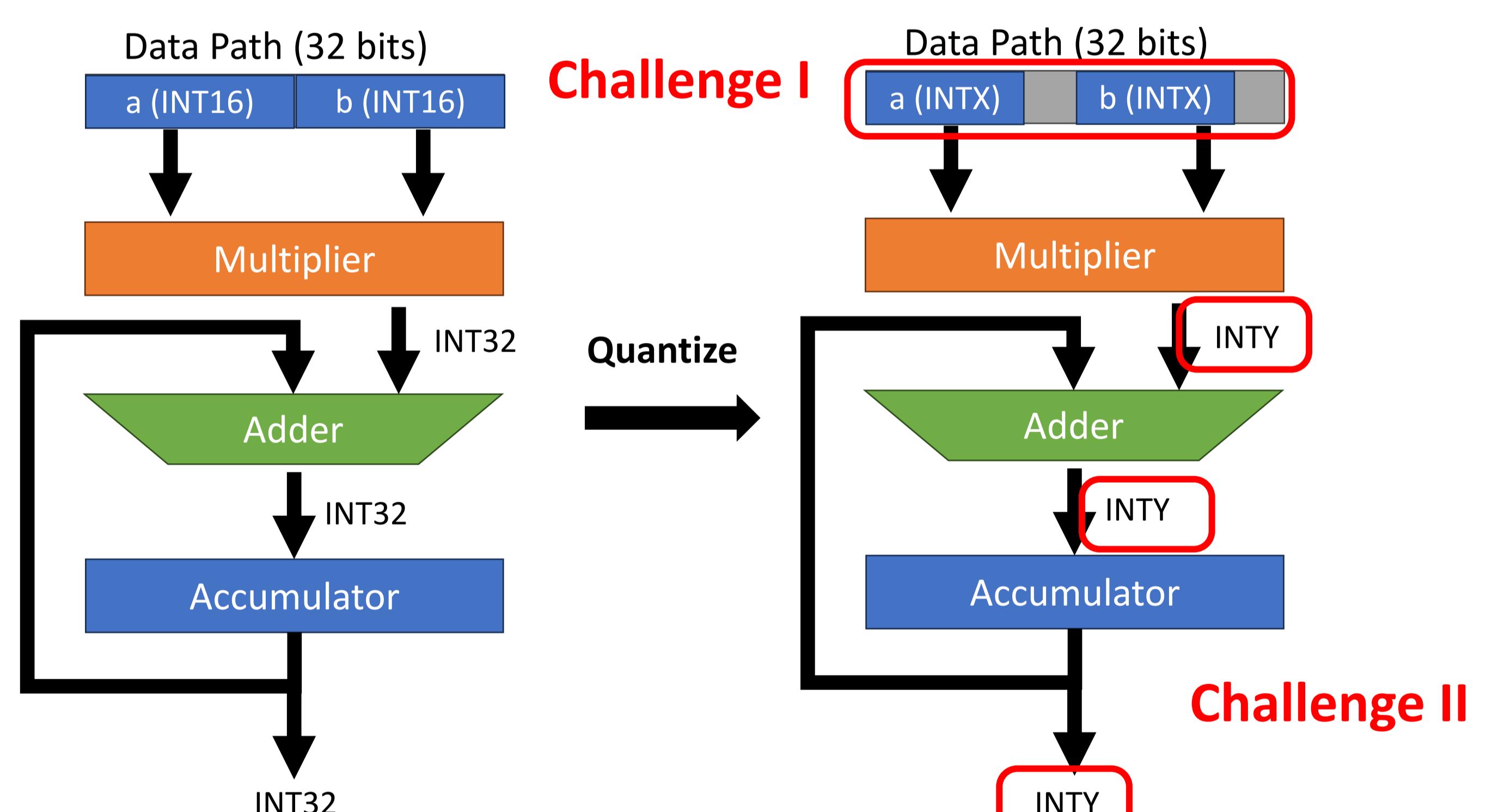
Objectives

- Save energy and reduce area
 - Simplifying the multipliers in fused multiply-add (FMA)
 - Reducing data transfer



Challenges

- (I) A data interface mismatch creates a bottleneck.
 - The bandwidth of the data path is wasted without optimization
- (II) The accumulator precision requires a careful trade-off



Potential Solutions

- Design new memory interfaces according to the input lengths
- Optimized batched data transfer strategies
- Select the accumulation precision by analyzing trade-off between accumulator precision, FMA precision, and overall model accuracy

III Conclusion

- Our experimental results indicate that standard INT8 precision is insufficient to preserve LLM inference accuracy.
- We show that supporting fine-grained, irregular bit lengths to maintain high LLM accuracy.
- However, implementing these irregular bit lengths efficiently in hardware presents significant challenges, which are left as our future studies.

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